

AMENDMENTS

In the Claims:

This listing of claims replaces all prior versions and listings of claims in this application:

1. (Previously Presented) An electroluminescent display device comprising:

a plurality of pixels;

a pixel selecting transistor provided for each of the pixels;

an electroluminescent element provided for each of the pixels; and

a driving transistor provided for each of the pixels to drive a corresponding electroluminescent element according to a display signal supplied through a corresponding pixel selecting transistor, the driving transistor comprising a channel of a P type and being of a lightly-doped-drain type.

2. (Currently Amended) The electroluminescent display device of claim 1, wherein the driving transistor further comprises a gate electrode disposed on the channel of the P-type, a P-type impurity region and a region of no doped impurities that is disposed between the gate electrode channel of the P-type and the P-type impurity region.

3. (Previously Presented) The electroluminescent display device of claim 1, wherein the driving transistor further comprises a high concentration region containing a P-type impurity with a concentration of $1\times10^{20}/\text{cc}$ or more and being in contact with an electrode, and a low concentration region containing a P-type impurity with a concentration of $1\times10^{18}/\text{cc}$ or less and disposed between the high concentration region and the channel of the P type.

4. (Previously Presented) The electroluminescent display device of claim 2, wherein the P-type impurity region comprises a high concentration region containing a P-type impurity with a concentration of $1\times10^{20}/\text{cc}$ or more and being in contact with an electrode, and a low concentration region containing a P-type impurity with a concentration of $1\times10^{18}/\text{cc}$ or less and disposed between the high concentration region and the channel of the P type.

5. (Previously Presented) The electroluminescent display device of claim 1, further comprising a glass substrate on which the pixels, the pixel selecting transistors, the electroluminescent elements and the driving transistors are disposed.